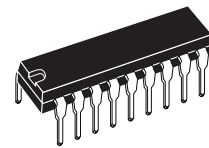




ULQ2801A  
ULQ2802A - ULQ2803A  
ULQ2804A - ULQ2805A

## EIGHT DARLINGTON ARRAYS

- EIGHT DARLINGTONS PER PACKAGE
- **EXTENDED TEMPERATURE RANGE**  
(- 40 to 105°C)
- OUTPUT CURRENT TO 500mA
- OUTPUT VOLTAGE TO 50V
- INTEGRAL SUPPRESSION DIODES
- VERSIONS FOR ALL POPULAR LOGIC FAMILIES
- OUTPUT CAN BE PARALLELED
- INPUTS PINNED OPPOSITE OUTPUTS TO SIMPLIFY BOARD LAYOUT



**DIP18**  
(Plastic Package)

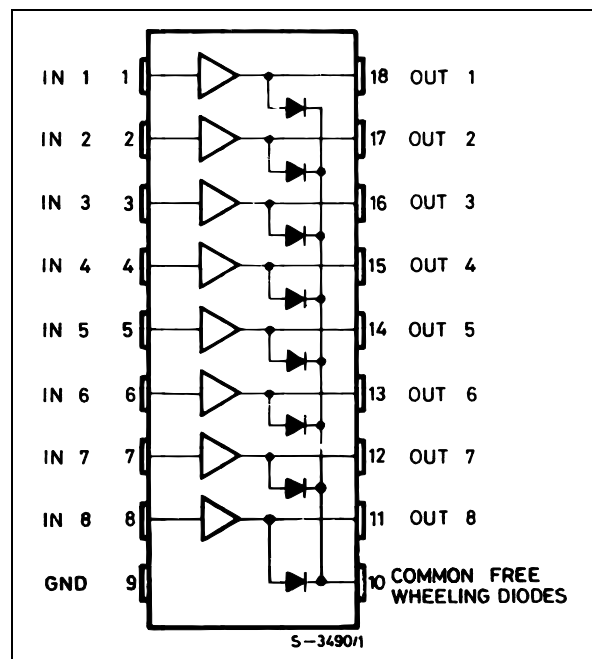
### DESCRIPTION

The ULQ2801A-ULQ2805A each contain eight darlington transistors with common emitters and integral suppression diodes for inductive loads. Each darlington features a peak load current rating of 600mA (500mA continuous) and can withstand at least 50V in the off state. Outputs may be paralleled for higher current capability.

Five versions are available to simplify interfacing to standard logic families : the ULQ2801A is designed for general purpose applications with a current limit resistor ; the ULQ2802A has a 10.5kΩ input resistor and zener for 14-25V PMOS ; the ULQ2803A has a 2.7kΩ input resistor for 5V TTL and CMOS ; the ULQ2804A has a 10.5kΩ input resistor for 6-15V CMOS and the ULQ2805A is designed to sink a minimum of 350mA for standard and Schottky TTL where higher output current is required.

All types are supplied in a 18-lead plastic DIP with a copper lead frame and feature the convenient input-opposite-output pinout to simplify board layout.

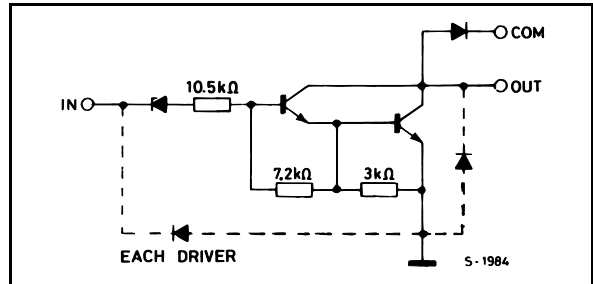
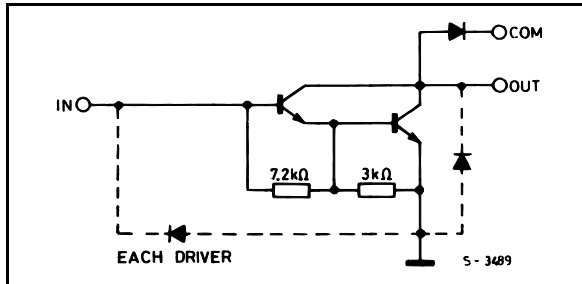
### PIN CONNECTION (top view)



# ULQ2801A - ULQ2802A - ULQ2803A - ULQ2804A - ULQ2805A

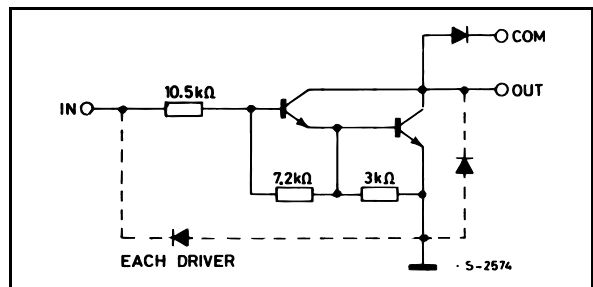
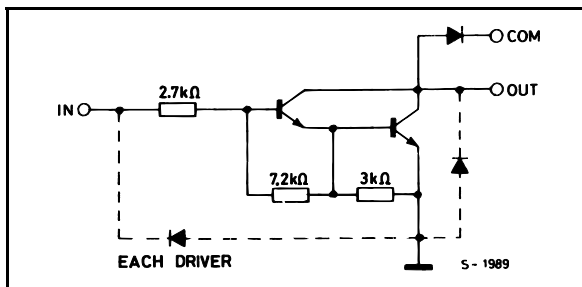
## SCHEMATIC DIAGRAM AND ORDER CODES

For ULQ2801A (each driver for PMOS-CMOS) For ULQ2802A (each driver for 14-15 V PMOS)

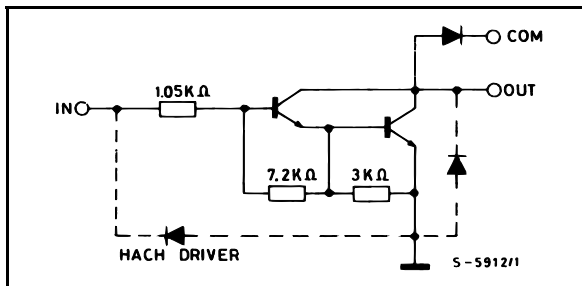


For ULQ2803A (each driver for 5 V, TTL/CMOS)

For ULQ2804A (each driver for 6-15 V CMOS/PMOS)



For ULQ2805A (each driver for high out TTL)



## ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
$V_o$	Output Voltage	50	V
$V_i$	Input Voltage for ULQ2802A, 2803A, 2804A for ULQ2805A	30 15	V V
$I_c$	Continuous Collector Current	500	mA
$I_B$	Continuous Base Current	25	mA
$P_{tot}$	Power Dissipation (one Darlington pair) (total package)	1.0 2.25	W W
$T_{amb}$	Operating Ambient Temperature Range	- 40 to 105	°C
$T_{stg}$	Storage Temperature Range	- 55 to 150	°C

## THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{th j-amb}$	Thermal Resistance Junction-ambient	Max. 55	°C/W

**ULQ2801A - ULQ2802A - ULQ2803A - ULQ2804A - ULQ2805A**

**ELECTRICAL CHARACTERISTICS** ( $T_J = -40$  to  $105^\circ\text{C}$ , unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit	Fig.
$I_{CEX}$	Output Leakage Current	$V_{CE} = 50V$ $T_J = 105^\circ\text{C}$ , $V_{CE} = 50V$			50	$\mu\text{A}$	1a
		$T_J = 105^\circ\text{C}$ for <b>ULQ2802A</b> $V_{CE} = 50V$ , $V_i = 6V$ for <b>ULQ2804A</b> $V_{CE} = 50V$ , $V_i = 1V$			100	$\mu\text{A}$	1a
$V_{CE(sat)}$	Collector-emitter Saturation Voltage	$I_C = 100\text{mA}$ , $I_B = 250\mu\text{A}$		0.9	1.1	V	2
		$I_C = 200\text{mA}$ , $I_B = 350\mu\text{A}$		1.1	1.3	V	
		$I_C = 350\text{mA}$ , $I_B = 500\mu\text{A}$		1.3	1.6	V	
$I_{i(on)}$	Input Current	for <b>ULQ2802A</b> $V_i = 17V$		0.82	1.25	$\text{mA}$	3
		for <b>ULQ2803A</b> $V_i = 3.85V$		0.93	1.35	$\text{mA}$	
		for <b>ULQ2804A</b> $V_i = 5V$		0.35	0.5	$\text{mA}$	
		for <b>ULQ2805A</b> $V_i = 12V$		1	1.45	$\text{mA}$	
$I_{i(off)}$	Input Current	$T_J = 105^\circ\text{C}$ , $I_C = 500\mu\text{A}$	50	65		$\mu\text{A}$	4
		for <b>ULQ2802A</b> $V_{CE} = 2V$ , $I_C = 300\text{mA}$			13	V	5
$V_{i(on)}$	Input Voltage	for <b>ULQ2803A</b> $V_{CE} = 2V$ , $I_C = 200\text{mA}$			2.4	V	
		for <b>ULQ2804A</b> $V_{CE} = 2V$ , $I_C = 250\text{mA}$			2.7	V	
			$V_{CE} = 2V$ , $I_C = 300\text{mA}$			3	
		for <b>ULQ2804A</b> $V_{CE} = 2V$ , $I_C = 125\text{mA}$			5	V	
			$V_{CE} = 2V$ , $I_C = 200\text{mA}$			6	
		for <b>ULQ2805A</b> $V_{CE} = 2V$ , $I_C = 275\text{mA}$			7	V	
			$V_{CE} = 2V$ , $I_C = 350\text{mA}$			8	
		for <b>ULQ2805A</b> $V_{CE} = 2V$ , $I_C = 350\text{mA}$			2.4	V	
$h_{FE}$	DC Forward Current Gain	for <b>ULQ2802A</b> $V_{CE} = 2V$ , $I_C = 350\text{mA}$	1000			–	2
$C_i$	Input Capacitance			15	25 (*)	pF	–
$t_{PLH}$	Turn-on Delay Time	$0.5 V_i$ to $0.5 V_o$		0.25	1 (*)	$\mu\text{s}$	–
$t_{PHL}$	Turn-off Delay Time	$0.5 V_i$ to $0.5 V_o$		0.25	1 (*)	$\mu\text{s}$	–
$I_R$	Clamp Diode Leakage Current	$V_R = 50V$ $T_J = 105^\circ\text{C}$ , $V_R = 50V$			50	$\mu\text{A}$	6
$V_F$	Clamp Diode Forward Voltage	$I_F = 350\text{mA}$		1.7	2	V	7

(\*) Guaranteed by design

TEST CIRCUITS

Figure 1a.

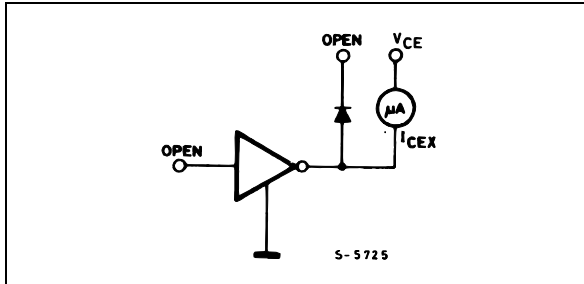


Figure 1b.

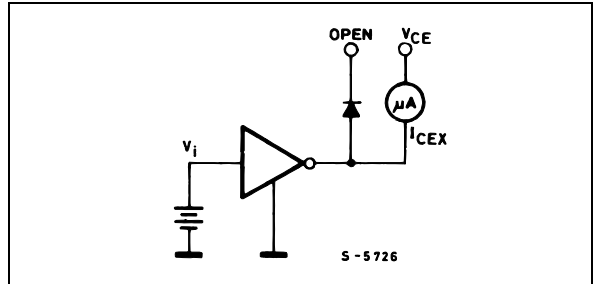


Figure 2.

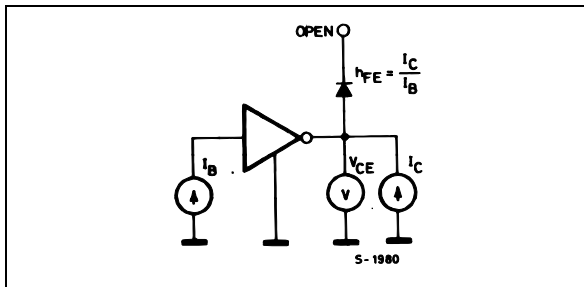


Figure 3.

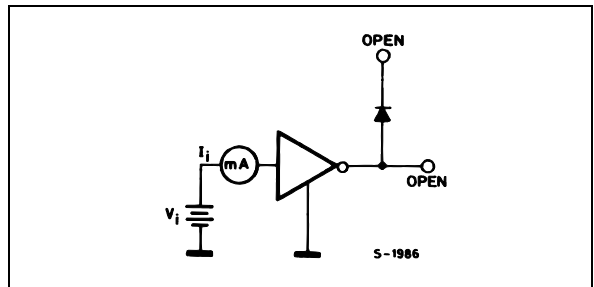


Figure 4.

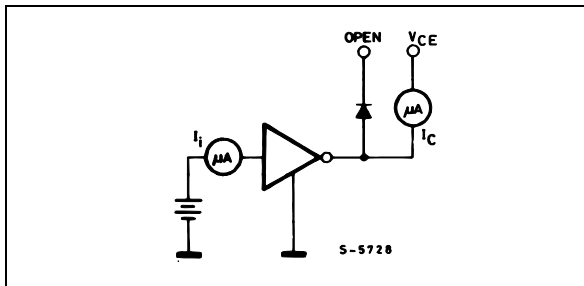


Figure 5.

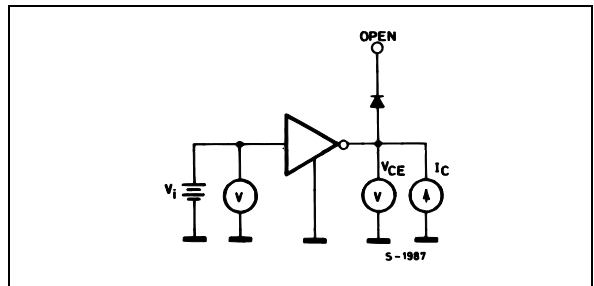


Figure 6.

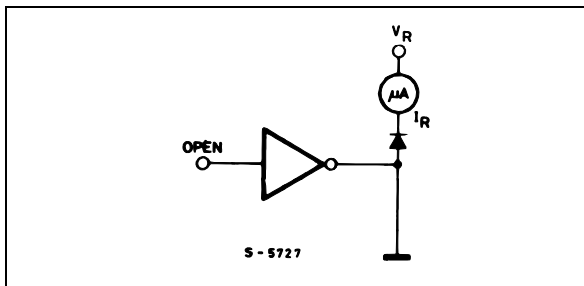


Figure 7.

